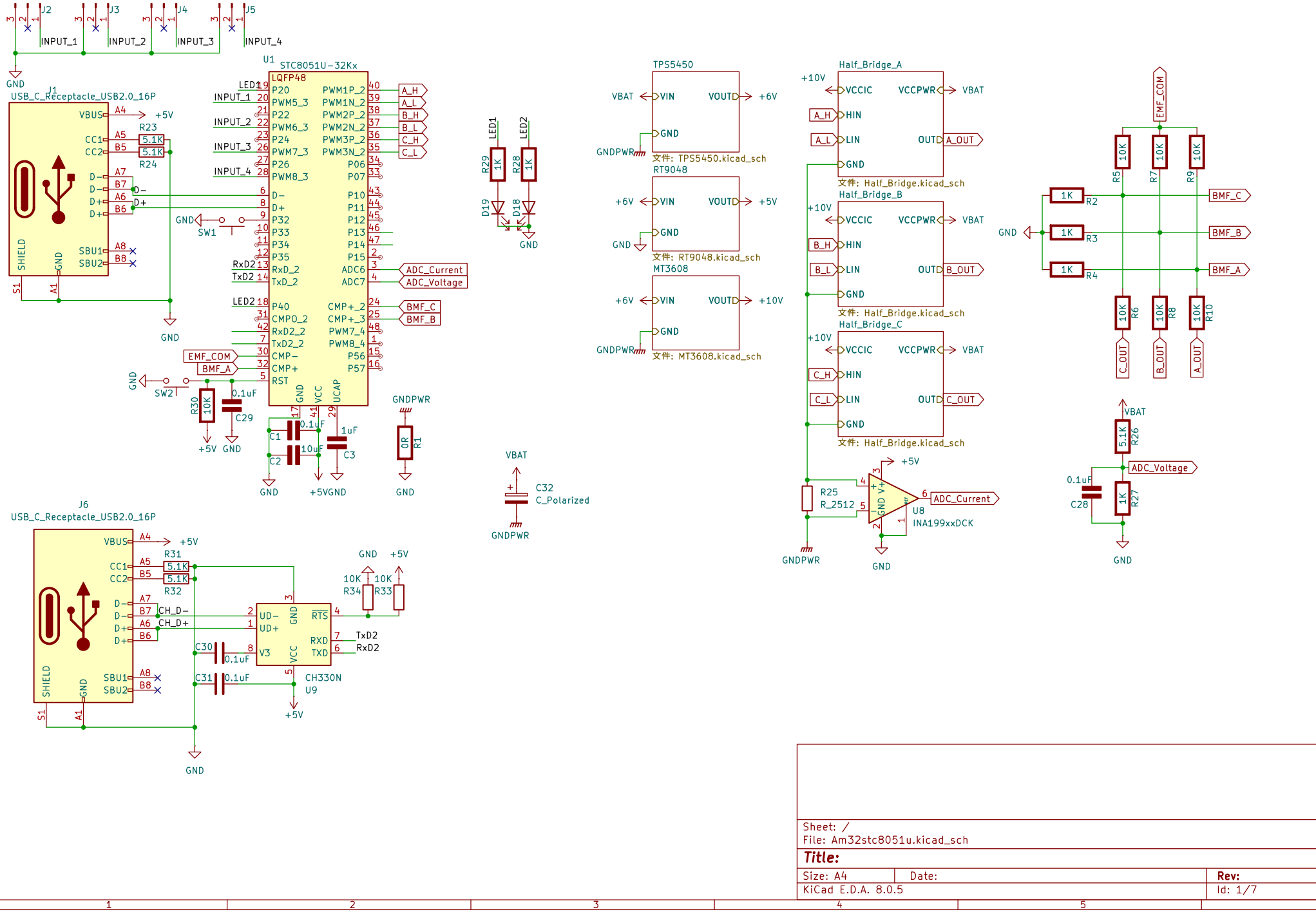
J2
J3
J4
J5
INPUT_1
INPUT_2
INPUT_3
INPUT_4
GND
U1
STC8051U-32Kx
LQFP48
J1
USB_C_Receptacle_USB2.0_16P
VBUS
CC1
CC2
D-
D+
SHIELD
GND
SBU1
SBU2
R23
R24
5.1K
+5V
S1
A1
SW1
SW2
EMF_COM
BMF_A
R30
10K
C29
0.1uF
+5V
GND
LED1
LED2
R29
R28
1K
D19
D18
RxD2
TxD2
ADC_Current
ADC_Voltage
BMF_C
BMF_B
C1
C2
C3
10uF
1uF
GNDPWR
OR
R1
+5VGND
J6
USB_C_Receptacle_USB2.0_16P
R31
R32
CH_D-
CH_D+
C30
C31
CH330N
U9
R34
R33
TxD2
RxD2
TPS5450
VBAT
VIN
VOUT
+6V
GNDPWR
文件: TPS5450.kicad_sch
RT9048
+6V
+5V
文件: RT9048.kicad_sch
MT3608
+10V
文件: MT3608.kicad_sch
Half_Bridge_A
Half_Bridge_B
Half_Bridge_C
+10V
VCCIC
VCCPWR
HIN
LIN
OUT
A_H
A_L
A_OUT
B_H
B_L
B_OUT
C_H
C_L
C_OUT
文件: Half_Bridge.kicad_sch
R25
R_2512
U8
INA199xxDCK
EMF_COM
BMF_C
BMF_B
BMF_A
R5
R7
R9
R2
R3
R4
R6
R8
R10
R26
R27
C28
0.1uF
C32
C_Polarized
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Rev:
KiCad E.D.A. 8.0.5
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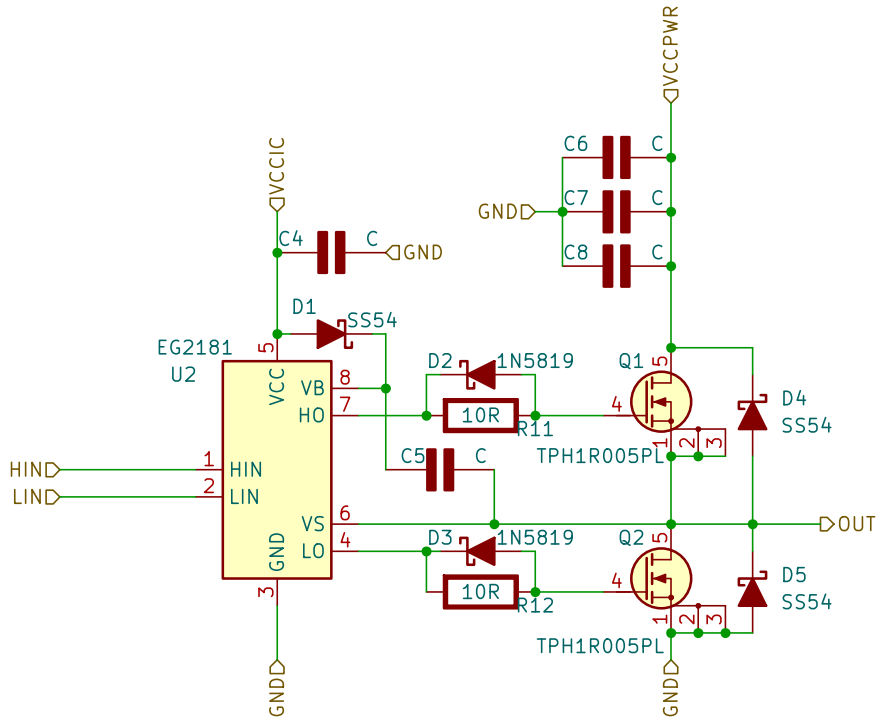

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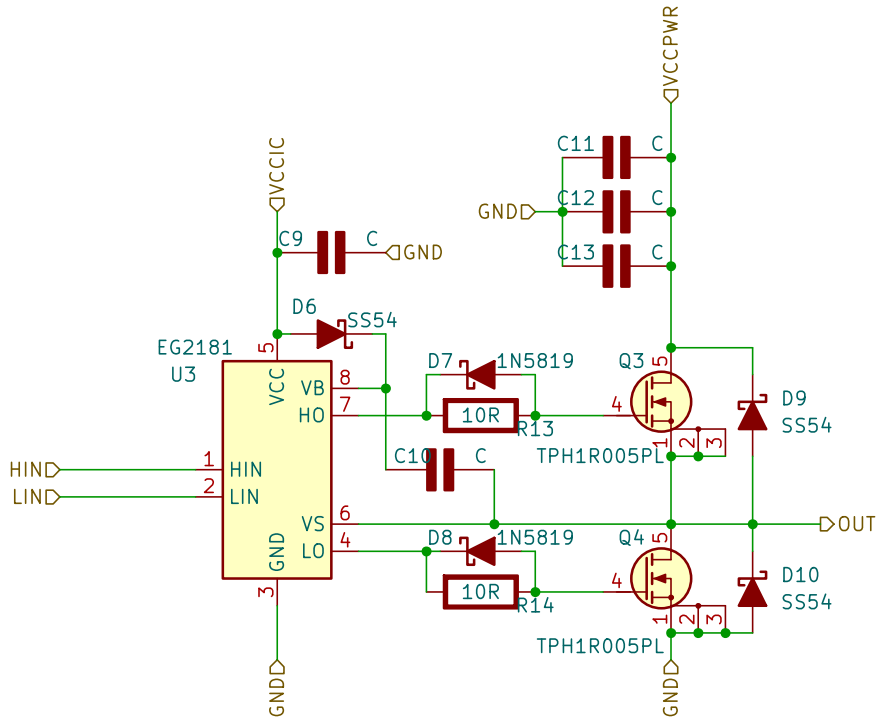

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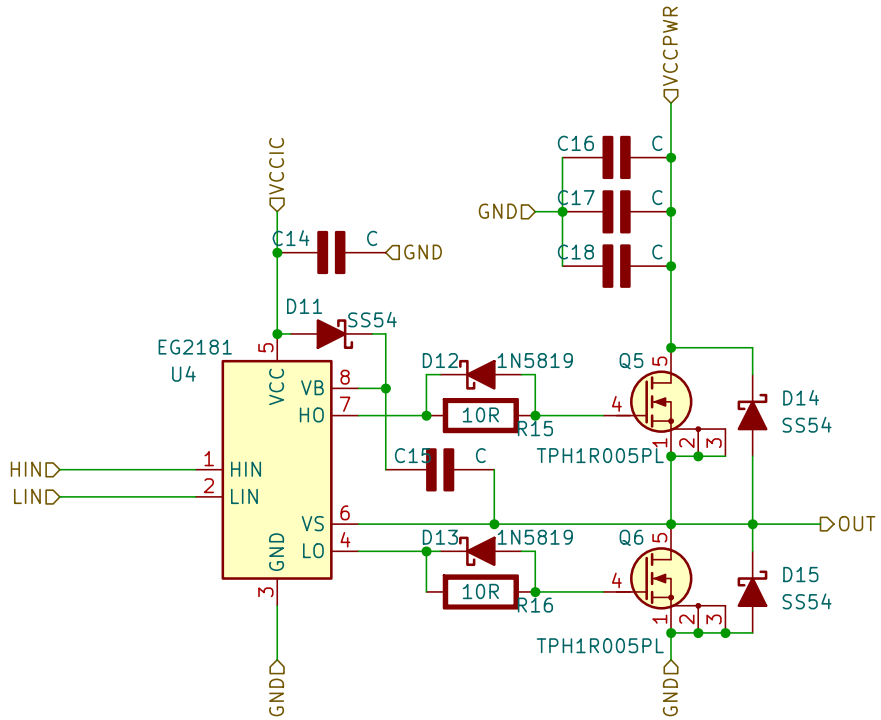

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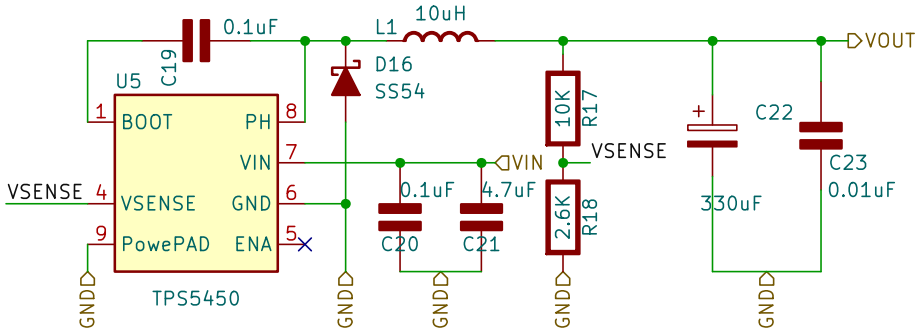

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KiCad E.D.A. 8.0.5		Id: 5/7

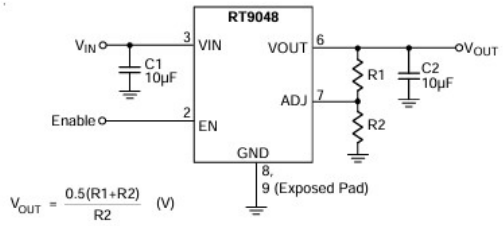
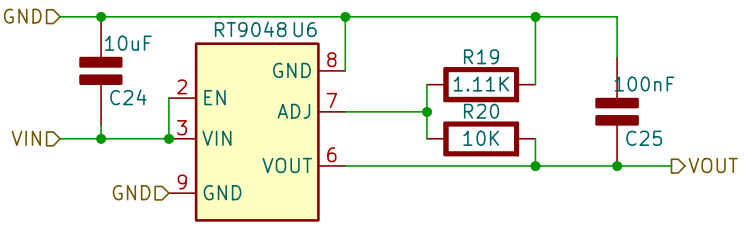


Figure 1. Adjustable Voltage Regulator



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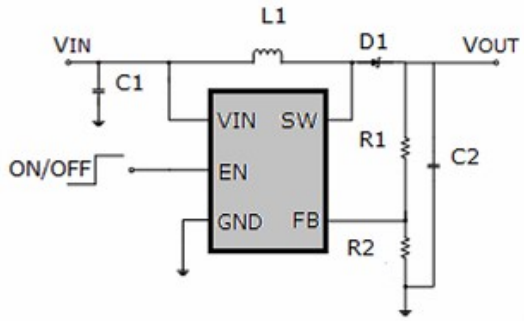
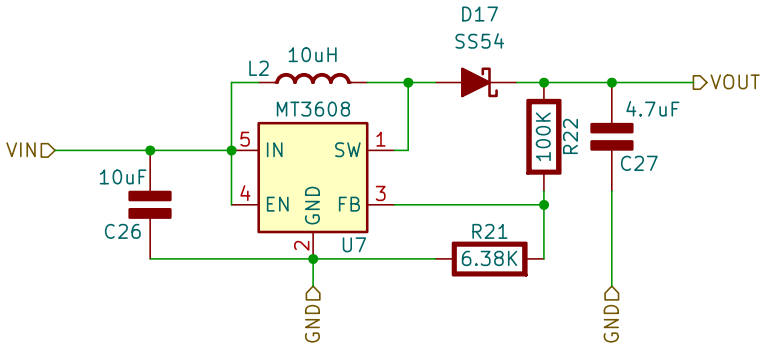


Figure 1. Basic Application Circuit

$$V_{OUT} = V_{REF} \times (1 + \frac{R_1}{R_2})$$

PIN	NAME	FUNCTION
1	SW	Power Switch Output. SW is the drain of the internal MOSFET switch. Connect the power inductor and output rectifier to SW. SW can swing between GND and 28V.
2	GND	Ground Pin
3	FB	Feedback Input. The FB voltage is 0.6V. Connect a resistor divider to FB.
4	EN	Regulator On/Off Control Input. A high input at EN turns on the converter, and a low input turns it off. When not used, connect EN to the input supply for automatic startup.
5	IN	Input Supply Pin. Must be locally bypassed.
6	NC	NC



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